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## CLAIMS

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1. A sense amplifier, comprising:
  - a. a sampling circuit receiving an input signal to the sense amplifier;
  - b. a reference node storing a reference signal corresponding to the input data;
  - c. a timing circuit activating the sampling circuit for a predetermined interval, the sampling circuit admitting the reference signal to the reference node thereby.
2. The sense amplifier of Claim 1, further comprising:
  - a. a pump capacitor substantially maintaining a value of the reference signal.
3. The sense amplifier of Claim 1, further comprising:
  - a. a gain circuit coupled with the reference node and disposed to adaptively adjust gain of an output signal produced by the sense amplifier.
4. The sense amplifier of Claim 1, wherein the sense amplifier is a single-ended sense amplifier.